

Notice of Allowability	Application No.	Applicant(s)	
	10/005,794	BROOKS ET AL.	
	Examiner	Art Unit	
	Kimberly E Glenn	2817	<i>Am</i>

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 4/22/04.
2. ☒ The allowed claim(s) is/are 1,3,4,6-9 and 11-21.
3. ☒ The drawings filed on 19 May 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: With regard to claims 1, 3,4,6,7,15-19 and 21, the prior art of record does not disclose or fairly teach a signal layer including a continuous signal conductor deposited on a first surface of a dielectric substrate, wherein the signal conductor is defined by a plurality of geometrically similar pathway portions that extend from a proximal end to a distal end of said signal conductor in adjoining relation to one another, wherein said plurality of pathway portions are positioned to electrically interact with each other, wherein first and second sections of said signal conductor that terminate at said proximal and distal ends exhibit a conductor width that is less than the conductor width of the intervening ones of said plurality of pathway portions and wherein the spacing between said first and second sections to the adjoining ones of said plurality of pathway portions is less than the spacing between the others of said plurality of pathway portions. With regards to claims 8, 9, 11 and 20, the prior art of record does not disclose or fairly teach a signal layer including a continuous signal conductor deposited on a first surface of a dielectric planar substrate, wherein the signal conductor is defined by a coiled pathway having a plurality of windings that extend from an outer end unbounded by said plurality of windings to a bounded interior end in parallel relation to one another, wherein the width of the portions of said windings terminating at said outer and interior ends is less than the width of the others of said plurality of windings. With regards to claims 12 and 13, the prior art of record does not disclose or fairly teach a signal layer including a continuous signal conductor deposited on a first surface of a dielectric planar substrate,

wherein the signal conductor is defined by a plurality of pathway portions that extend from a proximal end to a distal end in parallel relation to one another, wherein said plurality of pathway portions are positioned to electrically interact with each other, wherein first and second sections of the pathway portions terminate at said proximal and distal ends, wherein said first and second sections exhibit a tapering width that is less than the width of the others of said plurality of pathway portions and wherein the spacing between said first and second sections to the adjoining pathway portions is less than the spacing between the others of said plurality of pathway portions. With regards to claim 14, the prior art of record does not disclose or fairly teach a signal layer including a continuous signal conductor deposited on a first surface of a dielectric substrate, wherein the signal conductor is defined by a plurality of symmetrical pathway portions of identical shape that extend from a proximal end to a distal end in parallel relation to one another, wherein said plurality of pathway portions are positioned to electrically interact with each other, wherein the spacing between first and second sections of the pathway portions that terminate in said proximal and distal ends to the adjoining pathway portions is less than the spacing between the others of said plurality of pathway portions.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

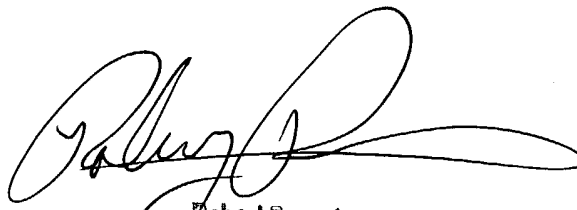
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimberly E Glenn whose telephone number is (571)-272-1761. The examiner can normally be reached on Monday-Friday 7:30 to 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571)-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kimberly E Glenn
Examiner
Art Unit 2817

keg



Robert Pascal
Supervisory Patent Examiner
Technology Center 2800